

TIME TABLE 2020-21

ACHARYA NARENDRA DEV COLLEGE

DEPARTMENT OF ELECTRONICS

B.Sc (H) Electronics IV Semester [CBCS]

PERIOD DAY	1st 8:45 a.m.– 9:45 a.m.	2nd 10:00 a.m.– 11:00a.m.	3rd 11:15a.m.– 12:15p.m.	4th 12:30 p.m.– 1:30p.m.	5th 1:30p.m. – 2:30p.m.	6th 2:30p.m. 3:30p.m.	7th 3:30p.m. 4:30 p.m.	8th 4:30p.m. 5:30p.m.
MON	Elect. Inst. DK	(SEC 1) G 2	Op-Amp. AG	Signals & Sys. VD	Op-Amp (B1)+ Signals and Sys.(B2)+ Electronic Inst.(B3) AA,VD,DK			
TUE		Elect. Inst. DK	Op-Amp. AG	Signals & Sys. VD	SEC 1 Lab(SEC) G2			
WED	Elect. Inst. DK	(SEC 1) G 2	Op-Amp. AG		Op-Amp (B2)+ Signals and Sys.(B3)+ Electronic Inst.(B1) AG,VD,DK			
THU	GE		Signals & Sys. VD		Op-Amp (B3)+ Signals and Sys.(B1)+ Electronic Inst.(B2) MB,VD,DK		Op-Amp (B3)+ Signals and Sys.(B1)+ Electronic Inst.(B2) AG,VD,DK	
FRI	Elect. Inst. DK	Signals & Sys. VD	Op-Amp. AG	GE	GE Practical			
SAT			GE					

AG- Dr. Amit Garg VD- Mr. Vishal Dhingra , DK- Mr. Dinesh Kumar MB- Dr. Monika Bhattacharya G2-Dr. Shikha Madan ,
B1,B2,B3- Three Batches

No Changes will be permitted without prior permission of the Principal